SLVS042D – JANUARY 1991 – REVISED JULY 1999

- Power-On Reset Generator
- Automatic Reset Generation After Voltage Drop
- Precision Input Threshold Voltage . . . 4.55 V ±120 mV
- Low Standby Current . . . 20 μA
- Reset Outputs Defined When V_{CC} Exceeds 1 V
- True and Complementary Reset Outputs
- Wide Supply-Voltage Range ... 1 V to 7 V

description

D. P. OR PW PACKAGE (TOP VIEW) RESET NC 8 NC RESET Π 2 7 NC [3 I NC 6 GND V_{CC} 5

NC - No internal connection

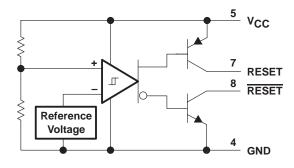
The TL7759 is a supply-voltage supervisor designed for use as a reset controller in microcomputer and microprocessor systems. The supervisor monitors the supply voltage for undervoltage conditions. During power up, when the supply voltage, V_{CC} , attains a value approaching 1 V, the RESET and RESET outputs become active (high and low, respectively) to prevent undefined operation. If the supply voltage drops below the input threshold voltage level (V_{IT-}), the reset outputs go to the reset active state until the supply voltage has returned to its nominal value (see timing diagram).

The TL7759C is characterized for operation from 0°C to 70°C.

AVAILABLE OPTIONS PACKAGED DEVICES CHIP SHRINK SMALL PLASTIC FORM TA SMALL OUTLINE DIP (Y) OUTLINE (D) (P) (PW) TL7759CD TL7759CP TL7759CPW 0°C to 70°C TL7759Y

The D and PW packages are available taped and reeled. Add the suffix R to the device type (e.g., TL7759CDR). Chip forms are tested at 25° C.

functional block diagram





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V _{CC} (see Note 1)	20 V
Off-state output voltage range: RESET voltage	
RESET voltage	–0.3 V to 20 V
Low-level output current, I _{OL} (RESET)	30 mA
High-level output current, I _{OH} (RESET)	–10 mA
Package thermal impedance, θ_{JA} (see Notes 2 and 3): D package	97°C/W
P package	127°C/W
PW package	149°C/W
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C
Storage temperature range, T _{stg}	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values are with respect to the network ground terminal.

- 2. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can impact reliability.
- 3. The package thermal impedance is calculated in accordance with JESD 51, except for through-hole packages, which use a trace length of zero.

recommended operating conditions

		MIN	MAX	UNIT
Supply voltage, V _{CC}		1	7	V
	Transistor off RESET voltage		15	V
Output voltage, V _O (see Note 4)	Transistor off RESET voltage	0		v
Low-level output current, I _{OL}	RESET		24	mA
High-level output current, IOH	RESET		-8	mA
Operating free-air temperature, T _A	TL7759C	0	70	°C

NOTE 4: RESET output must not be pulled down below GND potential.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED		TEAT OON	TEST CONDITIONS			TL7759C		
	PARAMETER		TEST CON	MIN	TYP‡	MAX	UNIT	
VOL	Low-level output voltage	RESET	V/aa - 4.2.V/	I _{OL} = 24 mA		0.4	0.8	V
VOH	High-level output voltage	RESET	V _{CC} = 4.3 V	I _{OH} = -8 mA	V _{CC} -1			V
\/. _	Input threshold voltage		$T_A = 25^{\circ}C$		4.43	4.55	4.67	V
VIT-	IT- (negative-going V _{CC})		$T_A = 0^{\circ}C$ to $70^{\circ}C$	4.4		4.7	v	
V 8			$R_1 = 2.2 k\Omega$	T _A = 25°C		0.8	1	V
V _{res} §	Power-up reset voltage		$R_{L} = 2.2 \text{ K}_{2}$	$T_A = 0^{\circ}C$ to $70^{\circ}C$			1.2	v
			$T_A = 25^{\circ}C$	40	50	60	mV	
V _{hys} ¶	Hysteresis at V _{CC} input		$T_A = 0^{\circ}C$ to $70^{\circ}C$		30		70	mv
ЮН	High-level output current	RESET		V _{OH} = 15 V			1	μΑ
IOL	Low-level output current	RESET	$V_{CC} = 7 V$, See Figure 1	$V_{OL} = 0 V$			-1	μΑ
	Supply ourrent		No load	V _{CC} = 4.3 V		1400	2000	
ICC	Supply current	oply current		V _{CC} = 5.5 V			40	μA

[‡] Typical values are at $T_A = 25^{\circ}C$.

§ This is the lowest voltage at which RESET becomes active, V_{CC} slew rate \leq 5 V/µs.

This is the difference between positive-going input threshold voltage, V_{IT+}, and negative-going input threshold voltage, V_{IT-}.



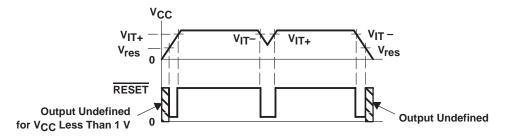
SLVS042D - JANUARY 1991 - REVISED JULY 1999

electrical characteristics, $T_A = 25^{\circ}C$ (unless otherwise noted)

							TL7759Y		
	PARAMETER		TEST C	ONDITIONS	MIN	TYP	MAX	UNIT	
VOL	Low-level output voltage	RESET	V _{CC} = 4.3 V,	I _{OL} = 24 mA		0.4		V	
V _{IT}	Input threshold voltage (negative-going				4.55		V		
V _{res} †	Power-up reset voltage		RL = 2.2 kΩ			0.8		V	
V _{hys} ‡	Hysteresis at V _{CC} input					50		mV	
ICC	Supply current		V _{CC} = 4.3 V,	No load		1400		μΑ	

[†] This is the lowest voltage at which $\overline{\text{RESET}}$ becomes active, V_{CC} slew rate $\leq 5 \text{ V/}\mu\text{s}$. [‡] This is the difference between positive-going input threshold voltage, V_{IT+} , and negative-going input threshold voltage, V_{IT-} .

timing diagram



switching characteristics at $T_A = 25^{\circ}C$ (unless otherwise noted)

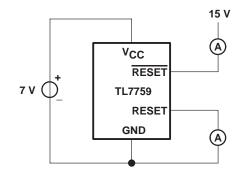
	PARAMETER	FROM	то	TEST CONDITIONS	TL77	UNIT	
	FARAMETER	(INPUT)	(OUTPUT)	TEST CONDITIONS	MIN	MAX	UNIT
^t PLH	Propagation delay time, low-to high-level output	VCC	RESET	See Figures 2 and 3§		5	μs
^t PHL	Propagation delay time, high-to low-level output	VCC	RESET	See Figures 2 and 4		5	μs
tr	Rise time		RESET	See Figures 2 and 4§		1	μs
t _f	Fall time		RESET	See Figures 2 and 4		1	μs
^t w(min)	Minimum pulse duration	VCC	RESET	See Figures 2 and 4	5		μs

 V_{CC} slew rate $\leq 5 V/\mu s$

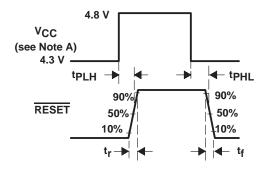


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PARAMETER MEASUREMENT INFORMATION

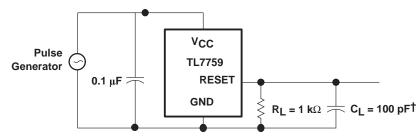




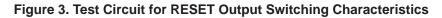


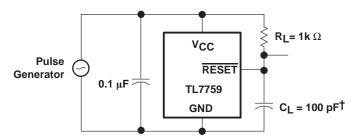
NOTE A: V_{CC} slew rate $\leq 5 V/\mu s$.

Figure 2. Switching Diagram



[†]C_L Includes jig and probe capacitance.





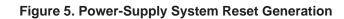
 $^{\dagger}C_{L}$ Includes jig and probe capacitance.





SLVS042D - JANUARY 1991 - REVISED JULY 1999

APPLICATION INFORMATION 5 V - 5 $0.1 \mu F - VCC$ RESET 7 TL7759 8 $\leq 1 k\Omega$





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4-Jun-2007

PACKAGING INFORMATION

JMENTS

www ti com

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TL7759CD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CDG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
TL7759CPE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
TL7759CPSR	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CPSRE4	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CPSRG4	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CPW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CPWE4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CPWG4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CPWLE	OBSOLETE	TSSOP	PW	8		TBD	Call TI	Call TI
TL7759CPWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CPWRE4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL7759CPWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS



compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION





QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*A	I dimensions are nominal												
	Device		Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
	TL7759CDR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
	TL7759CPSR	SO	PS	8	2000	330.0	16.4	8.2	6.6	2.5	12.0	16.0	Q1
	TL7759CPWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1



PACKAGE MATERIALS INFORMATION

19-Mar-2008



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TL7759CDR	SOIC	D	8	2500	340.5	338.1	20.6
TL7759CPSR	SO	PS	8	2000	346.0	346.0	33.0
TL7759CPWR	TSSOP	PW	8	2000	346.0	346.0	29.0

MECHANICAL DATA

MTSS001C - JANUARY 1995 - REVISED FEBRUARY 1999

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



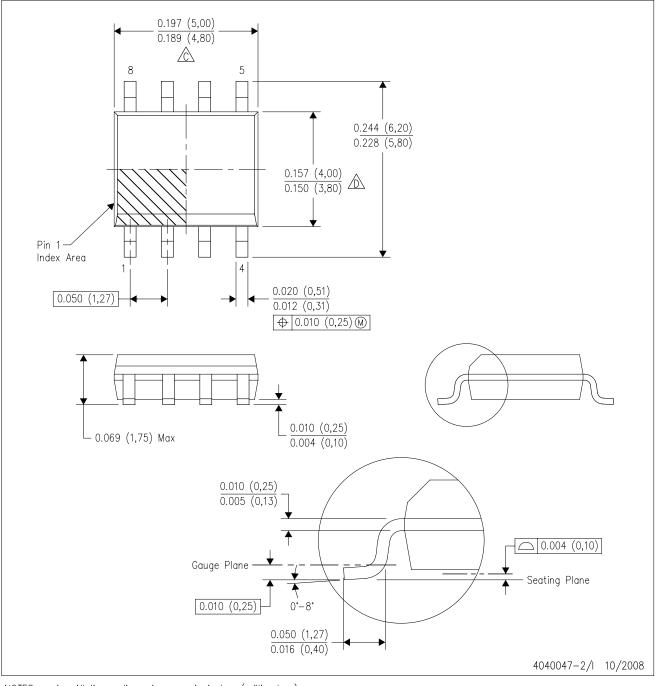
NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
- D. Falls within JEDEC MO-153



D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.

Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.

E. Reference JEDEC MS-012 variation AA.



MECHANICAL DATA

PS (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



MECHANICAL DATA

MPDI001A - JANUARY 1995 - REVISED JUNE 1999



- NOTES: A. All linear dimensions are in inches (millimeters).
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 - C. Falls within JEDEC MS-001

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